

Image



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2823
Examiner: William D. Coleman

In Re PATENT APPLICATION of:

Applicant(s) : Shigeyuki UEDA)

Serial No. : 09/665,663)

Filed : September 20, 2000)

For: SEMICONDUCTOR CHIP AND METHOD OF
PRODUCING THE SAME)

Attorney Ref. : AI 281)

FORM PTO-1449
CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a corrected Form PTO-1449. An Information Disclosure Statement and a Form PTO-1449 were filed on February 12, 2004. The PTO-1449 reflected an erroneous serial number. The proper serial number is indicated on the corrected attached PTO-1449. Consideration of the submitted document is respectfully requested.

Respectfully submitted,

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March 16, 2004
Date

SMR:pjl

PER ENCLOSED: \$
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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT		Atty. Docket		Application No.			
		AI 281		09/665,663			
		Applicant Shigeyuki UEDA					
		Filing Date September 20, 2000		Group 2823			
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub-Class	Translation
	AI	11-121509	4/30/99	JAPAN			Yes
	AJ	11-135714	5/21/99	JAPAN			Yes
	AK						
	AL						
	AM						
	AN						
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
	AO						
Examiner					Date Considered		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							